Amendments to the Specification:

Please replace the title as follows:

NOISE SUPRESSION CIRCUIT

NOISE SUPPRESSING CIRCUIT

Please replace the paragraph beginning on page 2, line 25, with the following rewritten paragraph:

The Published Unexamined Japanese Patent Application Heisei 8-213242 (1996) discloses a choke coil for reducing normal mode noise and common mode noise. The choke coil comprises a pair of windings, and first and second magnetic cores around which the pair of windings are wound. The first magnetic core is made of a low-permeability material while the second magnetic core is made of a high-permeability material. A specific gap is created between the first and second magnetic cores. In this choke coil, when a current of common mode noise is fed through the pair of windings, a magnetic flux produced by the windings attenuates in the second magnetic core, and common mode noise is thereby reduced. When a current of normal mode is fed through the pair of windings in the choke coil, a magnetic flux produced by the windings attenuates in the first magnetic core, and normal mode noise is thereby reduced.

Please replace the paragraph beginning on page 20, line 8, with the following rewritten paragraph:

Since the denominator of the right side of the equation (7) includes $\omega^2 \cdot M^2$, the current 'i2' is reduced by increasing the mutual inductance M. As shown in the equation (1), the coupling coefficient K is proportional to the mutual inductance M. Therefore, if the coupling coefficient K is increased, the effect of suppressing common mode signals-noise by

the circuit of FIG. 4 is enhanced. Since the mutual inductance M is included in a form of square in the denominator of the right side of the equation (7), the effect of suppressing common mode noise greatly varies, depending on the value of coupling coefficient K.

Please replace the paragraph beginning on page 22, line 3, with the following rewritten paragraph:

The noise suppressing circuit further comprises: a first winding W11 inserted to the conductor line 3 at a specific first point P11a; a magnetic core 11; a second winding W12 that is inserted to the conductor line 4 at a point P11b corresponding to the first point P11a and coupled to the first winding W11 through the core 11, and that suppresses common mode noise in cooperation with the first winding W11; and a third winding W13 coupled to the first winding W11 and the second winding W12 through the core 11. The windings W11 and W12 and the core 11 make up a common mode choke coil. That is, the windings W11 and W12 are wound around the core 11 in such directions that, when magnetic fluxes are induced in the core 11 by currents flowing through the windings W11 and W12 when a normal mode current is fed to the windings W11 and W12, these fluxes are cancelled out by each other. The windings W11 and W12 thereby suppress common mode noise and allow normal mode noise to pass. The numbers of turns of the windings W11, W12 and W13 are equal, for example.

Please replace the paragraph beginning on page 23, line 25, with the following rewritten paragraph:

The noise suppressing circuit further comprises a capacitor 16 for the normal mode having an end connected to the conductor line 3 at a point between the terminal 1a and the third point P13a and having the other end connected to the conductor line 4 at a point

between the terminal 1b and the point P13b corresponding to the terminal 1b and the third point P13a. In the example shown in FIG. 1, in particular, one of the ends of the capacitor 16 is connected to the conductor line 3 at a point between the second point P12a and the third point P13a, and the other of the ends of the capacitor 16 is connected to the conductor line 4 at a point between the point P12b corresponding to the second point P12a and the point P13b corresponding to the third point P13a.